

DAC1201D125:

Dual 12Bit DAC; Up to 125 Msps

Product Feature Sheet

Features

- Dual 12-bit resolution
- 125 Msps update rate
- Single 3.3 V supply
- Typical 185 mW power dissipation
- 16 mW power-down
- SFDR: 81 dBc; $f_o = 1$ MHz; $f_s = 52$ Msps
- Dual-port or Interleaved data modes
- SFDR: 78 dBc; $f_o = 10.4$ MHz; $f_s = 78$ Msps
- 1.8 V, 3.3 V and 5 V compatible digital inputs
- SFDR: 74 dBc; $f_o = 1$ MHz; $f_s = 52$ Msps; -12 dBFS
- Internal and external reference
- LQFP48 package
- 2 mA to 20 mA full-scale output current
- Industrial temperature range of -40 °C to +85 °C

Typical Applications

- Quadrature modulation
- Medical/test instrumentation
- Direct IF applications
- Direct digital frequency synthesis
- Arbitrary waveform generator

Device Overview

The DAC1201D125 is a dual-port, high-speed, 2-channel CMOS Digital-to-Analog Converter (DAC), optimized for high dynamic performance with low power dissipation. Supporting an update rate of up to 125 Msps, the DAC1201D125 is suitable for Direct IF applications.

Separate write inputs allow data to be written to the two DAC ports independently of one another. Two separate clocks control the update rate of each DAC port.

The DAC1201D125 can interface two separate data ports or one single interleaved high-speed data port. In Interleaved mode, the input data stream is demultiplexed into its original I and Q data and latched. The I and Q data is then converted by the two DACs and updated at half the input data rate.

Each DAC port has a high-impedance differential current output, suitable for both single-ended and differential analog output configurations.

The DAC1201D125 is pin compatible with the AD9765, DAC2902 and DAC5662.

Absolute Maximum Ratings:

Digital Supply Voltage	-0.3 V to +5.0 V
Analog Supply Voltage	-0.3 V to +5.0 V
Supply Voltage Difference	-50 mV to +150 mV
Input Voltage (digital inputs referenced to DGND)	-0.3 V to +5.5 V
Input Voltage (pins REFIO, AVIRES, BVIRES referenced to AGND)	-0.3 V to 5.5 V
Output Voltage (pins IOUTAP, IOUTAN, IOUTBP and IOUTBN referenced to AGND)	-0.3 V to $V_{DDA} + 0.3$ V
Storage Temperature	-55 °C to +150 °C
Ambient Temperature	-40 °C to +85 °C
Junction Temperature	125 °C

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Block Diagram

