



# 80HCPS1432xxxx-CED: 14 Port, 32 Lane Serial RapidIO® Gen2 Switch

## Product Feature Sheet

### Features

- Designed to Serial RapidIO 2.1 specification
- 32 lanes, with up to 8x4, 14x2, 14x1 port configurations
- Full-duplex 160 Gbps non-blocking bandwidth
- Carrier-grade, high performance SerDes
  - 1.25, 2.5, 3.125, 5.0, or 6.25 Gbaud
  - Long reach 100 cm with 2 connectors
  - Transmit drive strength and pre-emphasis
  - Receive equalization with DFE
- Up to 40% power-per-gigabit savings vs. RapidIO 1.3 Switches
- Dynamic ingress and egress buffers management improves performance over RapidIO 1.3 switches
  - Better per-port throughput
  - Better system-level traffic engineering
- 40 multicast groups per port
- Cut-through and Store-and-Forward modes
- Cut-through latency of 100 ns
- RapidIO Error Management Extension Support
- Error log captures sequence of errors
- Packet mirror, trace, filter per port
- Receiver- and Transmitter-based flow control
- Per-port reset provides robust hot swap support
- Multicast event control symbol generation input pin
- Industrial and Military temperature grades

### Screening / Quality / Packaging

- Up screening based on the Silicon360 certified enhanced device CED process
- Customized screening e.g. radiation, electrical, temperature, etc. if required
- Military temperature range -55 °C to 125 °C
- Ceramic packaging capability

### Packaging

- 25 x 25 mm FCBGA

### Device Overview

The 80HCPS1432xxxx-CED provides a full non-blocking bandwidth of 160 Gbps on up to 14 logical ports. The 80HCPS1432xxxx-CED uses a 5th generation switch fabric, building upon Gen 1 CPS and Tsi switching architectures. This new switch uses patent pending features to minimize latency, ensure scheduling fairness, and provide superior multicast throughput. The C80HCPS1432xxxx-CED's 6.25 Gbaud SerDes is ideal for backplanes yet can run even lower power for local connectivity. This performance is realized over twice the transmission channel distance and three orders of magnitude improvement in bit error rate (BER) compared to the very capable Gen1 standard.

IDT's Gen2 switches connect the Serial RapidIO ecosystem, including full backward compatibility to RapidIO 1.3 systems and components. The Serial RapidIO ecosystem enables carrier-grade reliable, fault tolerant systems with four standard traffic priority levels for quality of service. It also offers best-in-class messaging and atomic transaction support with protocol throughput efficiencies superior to other leading embedded interconnect protocols.

### Benefits

- Design highest performance backplane in industry with 20 Gbps data rate per link
- Lowest power per payload bit vs. other interconnect protocols
- Ecosystem support for four levels of priority plus critical request flow, providing up to eight classes of traffic
- RapidIO standard supports arbitrary system topology with true peer-to-peer networking
- Twice the performance per link compared to 10 Gb Ethernet
- RapidIO Messaging Support for transfers of 4 KB messages in hardware

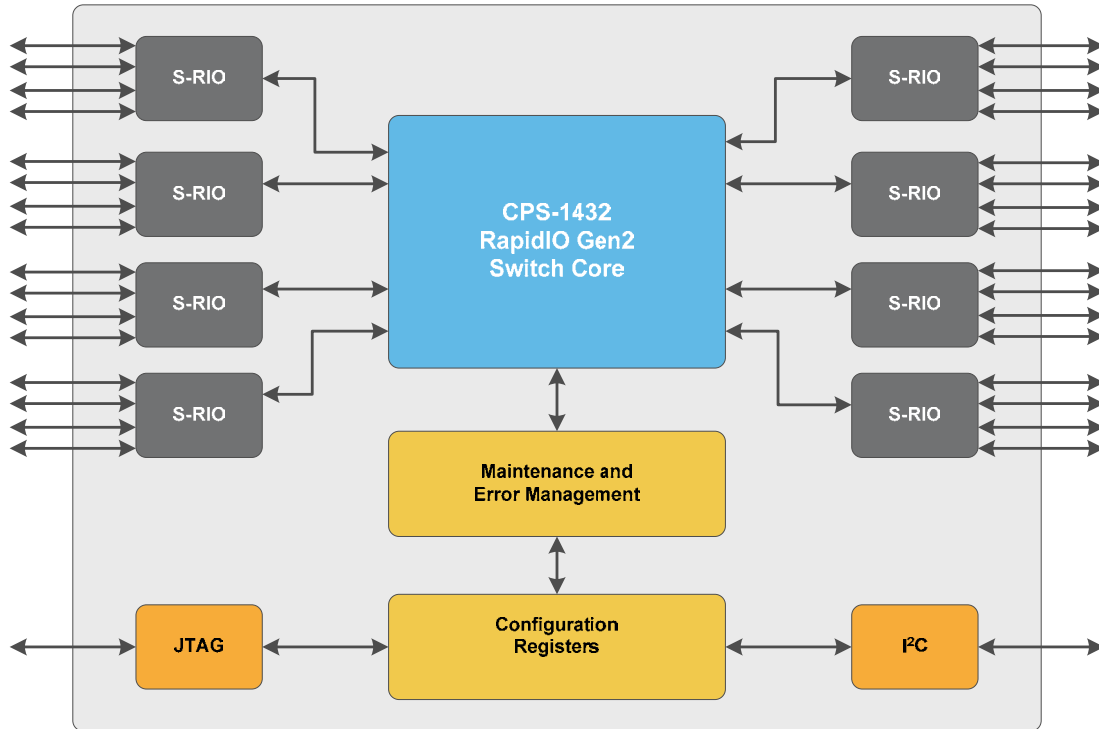
### Typical Applications

- Wireless: Baseband cards and backplanes in LTE/WiMAX/WCDMA/TD-SCDMA
- Defense and aerospace: Radar, sonar and navigations systems
- Medical imaging: CT scanners, MRIs
- Video: Teleconferencing and Head End
- Industrial control systems

### Ordering Information

80HCPS1432CHMI-CED	Industrial Temp
80HCPS1432CRMI-CED	Industrial Temp
80HCPS1432CHME-CED	Extended Temp
80HCPS1432CRME-CED	Extended Temp

Functional Block Diagram



### Benefits for Wireless

- Carrier-grade reliable packet transport
- Gen2 performance to power ratio allows unprecedented compute density to enable 3 G and 4 G systems
- Switched architecture allows highly scalable system for micro and macro BTS implementations
- Carrier-grade 6.25 Gbaud SerDes enables backplane-based modular systems and system scaling by inter-chassis cabling
- Ecosystem-standard support for four priorities plus Critical Request Flow provides strong QoS support for multiple data flows plus control plane

### Benefits for Video and Imaging

- 40 multicast masks per port provides strong support for broadcasting or multicasting a given data stream to multiple endpoints executing unique transforms, scaling, and CODECs
- IDT-proprietary “retransmit mimic” feature improves real-time support for latency sensitive lossy data transfers

### Benefits for Defense and Aerospace

- Serial RapidIO Error Management Extension support including Time-to-Live enables fault-tolerant systems
- VITA 41, Open VPX, and ATCA fabric mappings enable rapid development of modular, standards-based systems
- RapidIO-standard, true peer-to-peer networking allows scaling of arbitrary topology and simplifies hot swap software implementation
- Per-port filter feature allows blocking errant packets or malicious attack (for example, denial of service, system memory reads and writes)

### Software and Hardware Ecosystem

- Serial RapidIO Development Platform Gen2 (SRDP2)
- Serial RapidIO Gen2 Endpoint Intellectual Property for ASIC, CPU, DSP, and FPGAs
- Numerous partner RapidIO-enabled AMCs
- RapidFET JTAG edition software support
- RapidIO Linux support
- Power Calculator tool
- HSPICE and IBIS models